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Application / Conf. No. 10/627,335 2274 TRANSMITTAL Filing Date July 24, 2003 **FORM** First Named Inventor Vincent J. Jorgensen (to be used for all correspondence after initial filing) **Examiner Name** Eun Hee Chung Art Unit Mail Stop: $_{f AF}$ 2123 Patent No. Express Mail Receipt No. Attorney Docket Number X-1251 US Total Number of Pages in This Submission

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X-1251 US 10/627,335 PATENT

Conf. No.: 2274

IN THE UNITED STATES PATENT OFFICE

Applicants:

Vincent J. Jorgensen et al.

Assignee:

Xilinx, Inc.

Title:

Dummy Block Replacement for Logic Simulation

Serial No.:

10/627,335

File Date:

07-24-2003

Examiner:

Eun Hee Chung

Art Unit:

2123

Docket No.:

X-1251 US

Conf. No.:

2274

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AMENDMENT IN RESPONSE TO THE FINAL OFFICE ACTION

Dear Sir:

In response to the Final Office Action mailed from the Patent Office on January 12, 2007, please replace/substitute the following claims as indicated.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 7 of this paper.